



11/28/00

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

	O.I.P.E.	PATENT DATE
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APPLICATION NO. 09/724972	CONT/PRIOR	CLASS <sup>712</sup> 365	SUBCLASS	ART UNIT <del>2818</del> 2818	EXAMINER <i>M. S. [Signature]</i>
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## APPLICANTS

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**TITLE**

Programmable logic device with decryption and structure for preventing design relocation

PTO-2040  
12/99

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  _____  _____				<b>ISSUE FEE</b>	
	_____ (Primary Examiner) _____ (Date)	Amount Due	Date Paid		
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	
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